

Unbuffered divider.

Figure 1

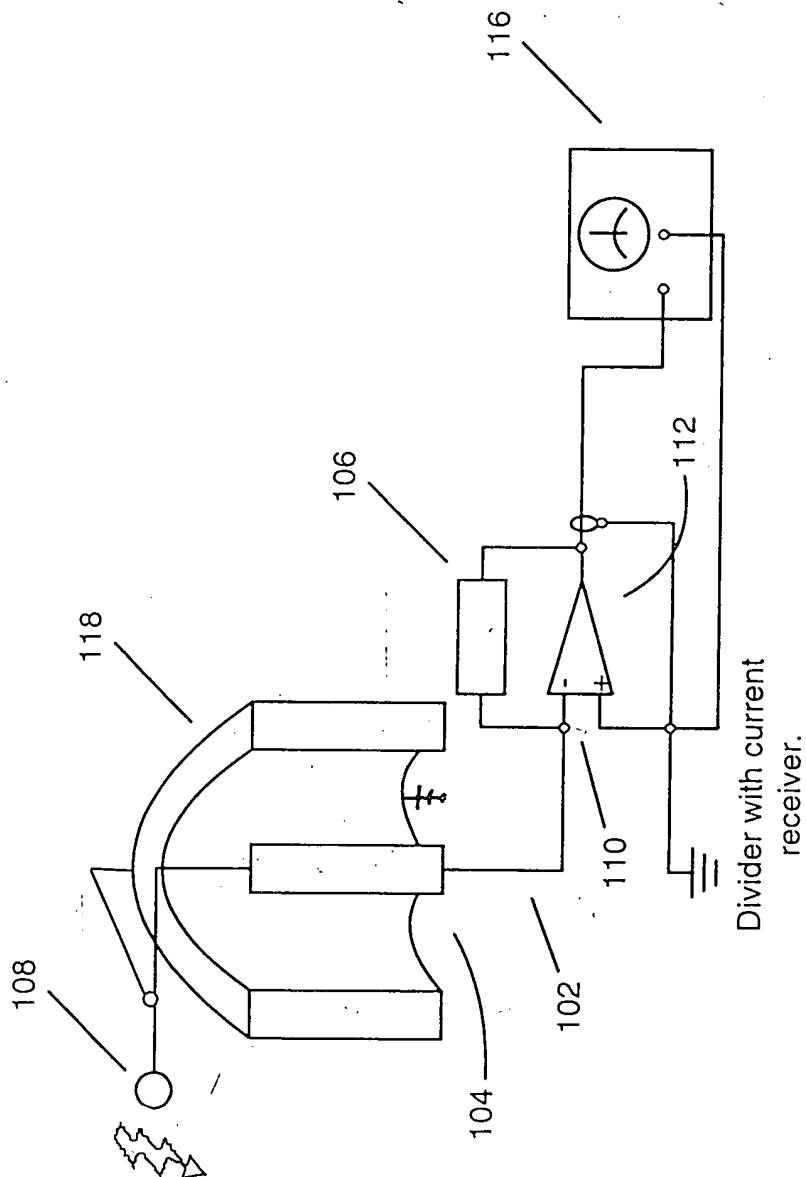


Figure 2

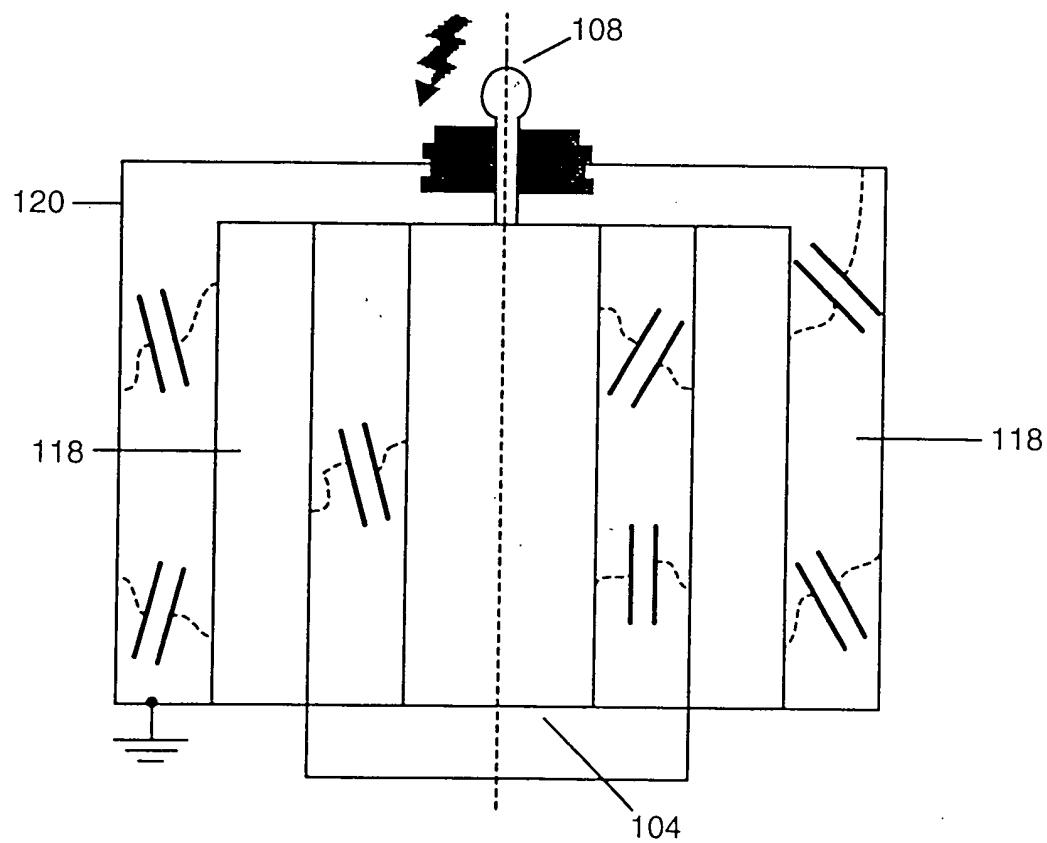


Figure 3

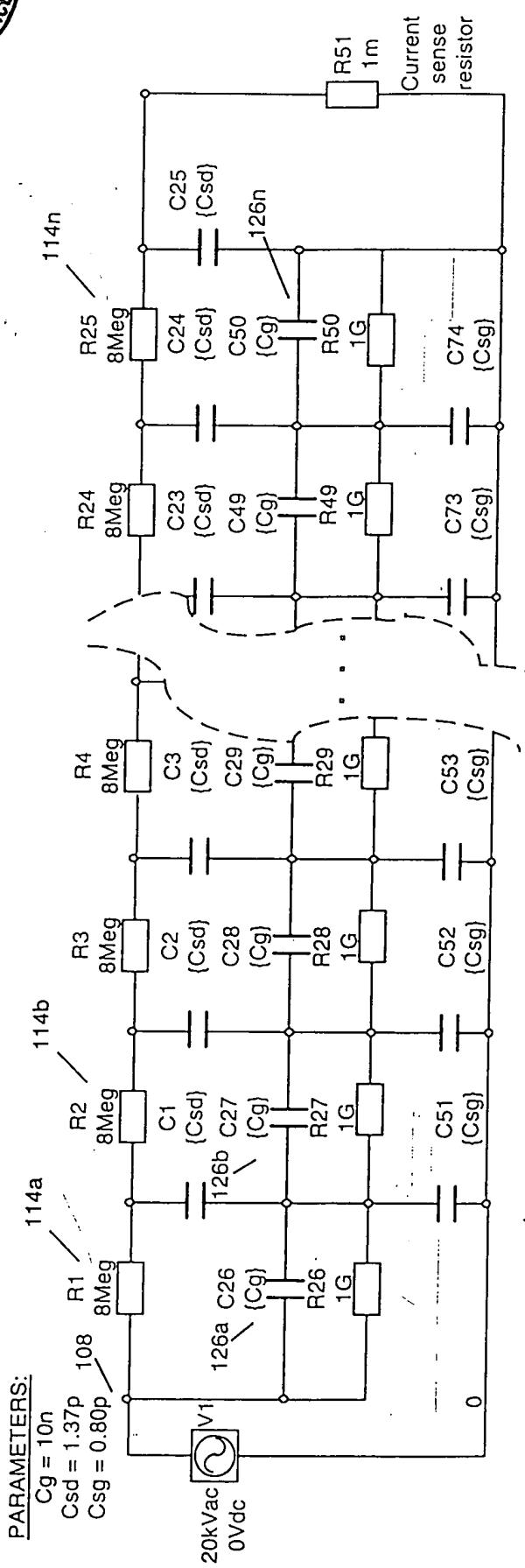


Figure 4

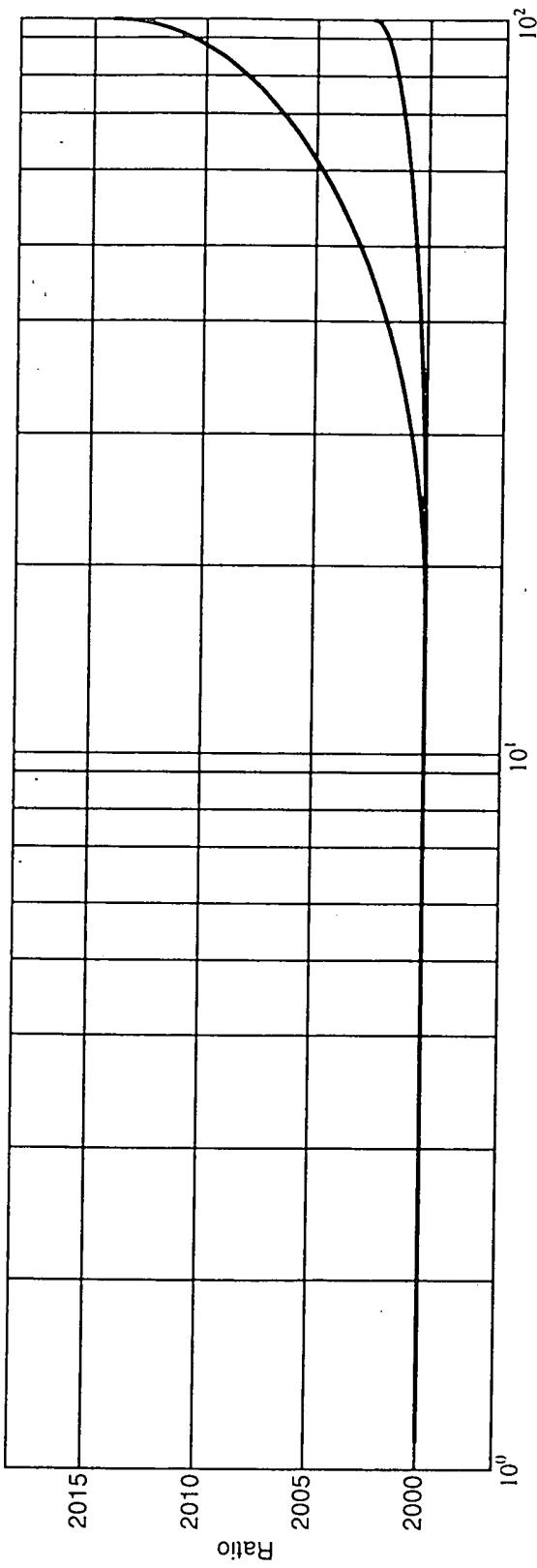
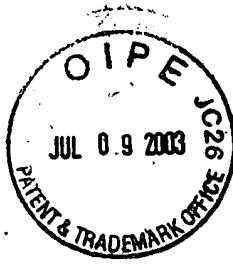


Figure 5

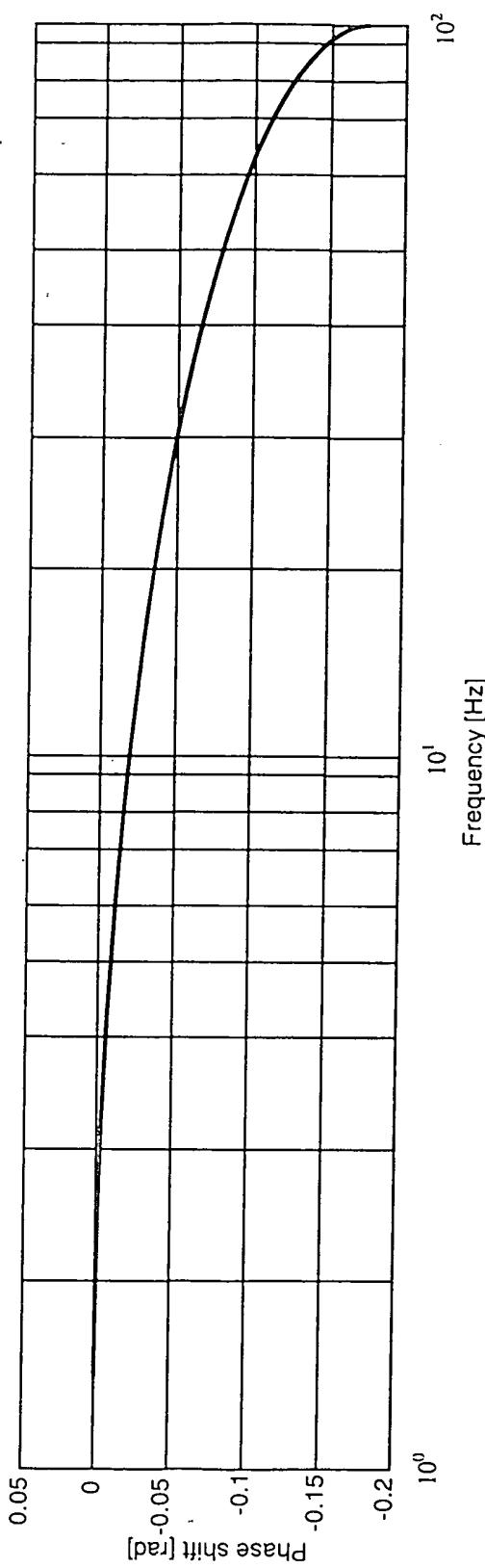
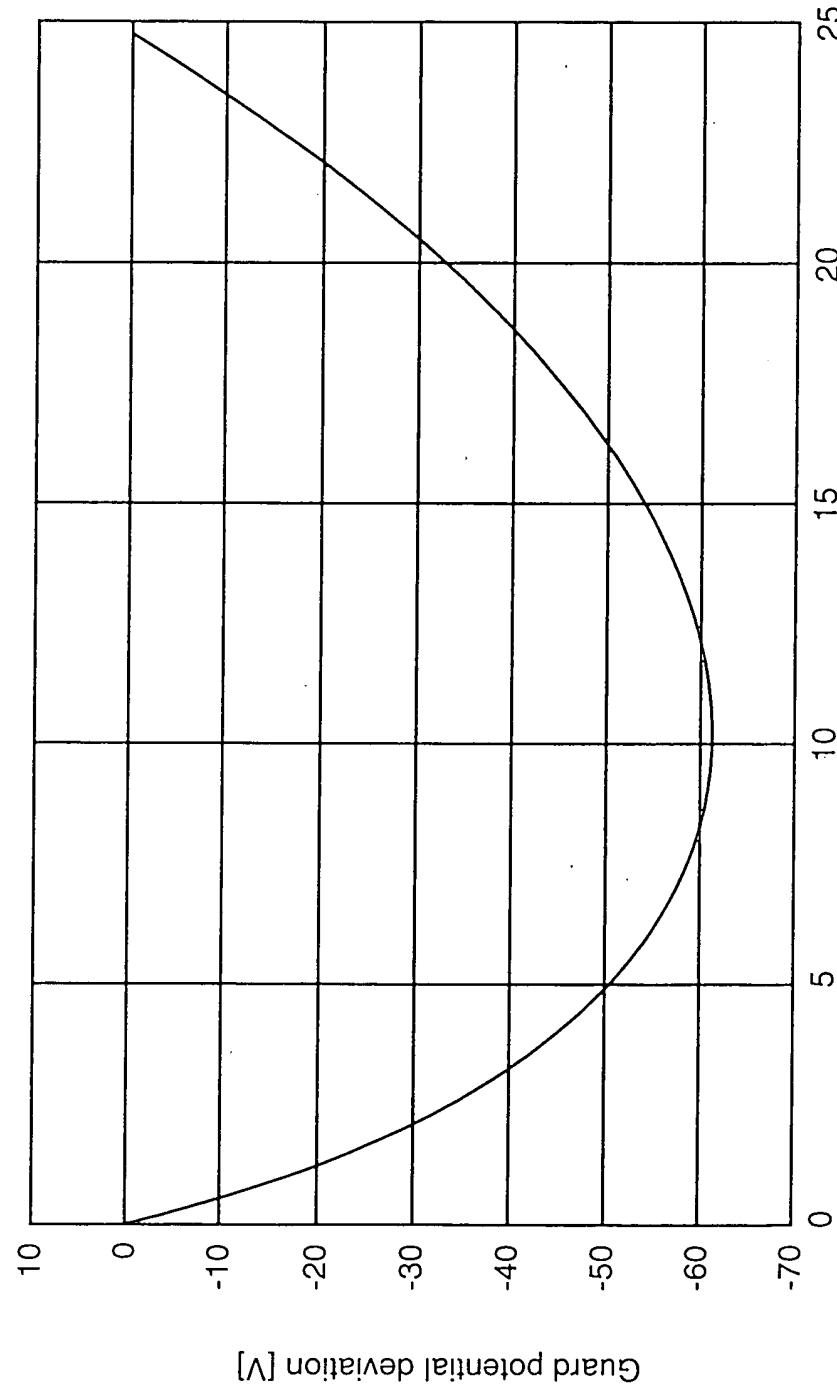


Figure 6



Guard section
Deviation of the guard potential from ideal potential

Figure 7

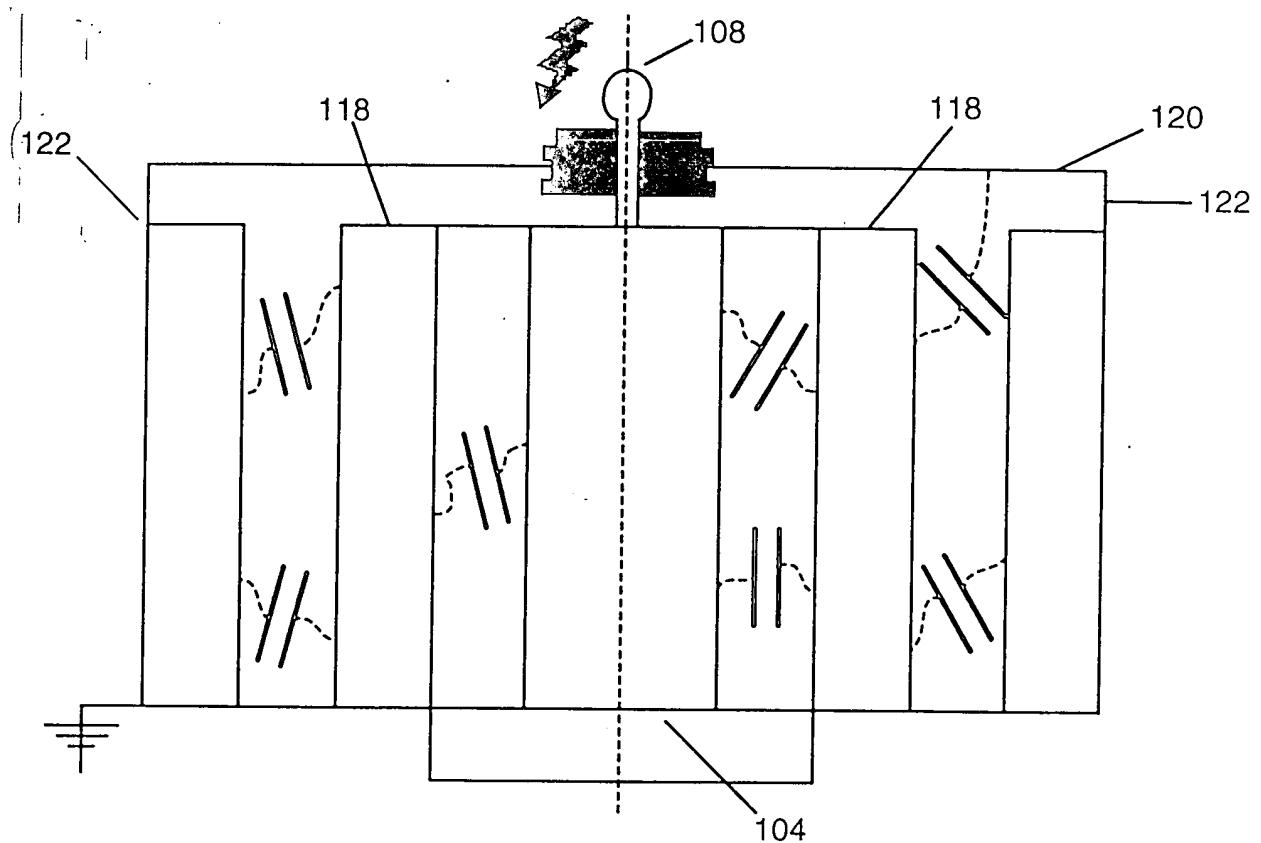
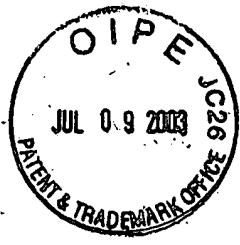


Figure 8

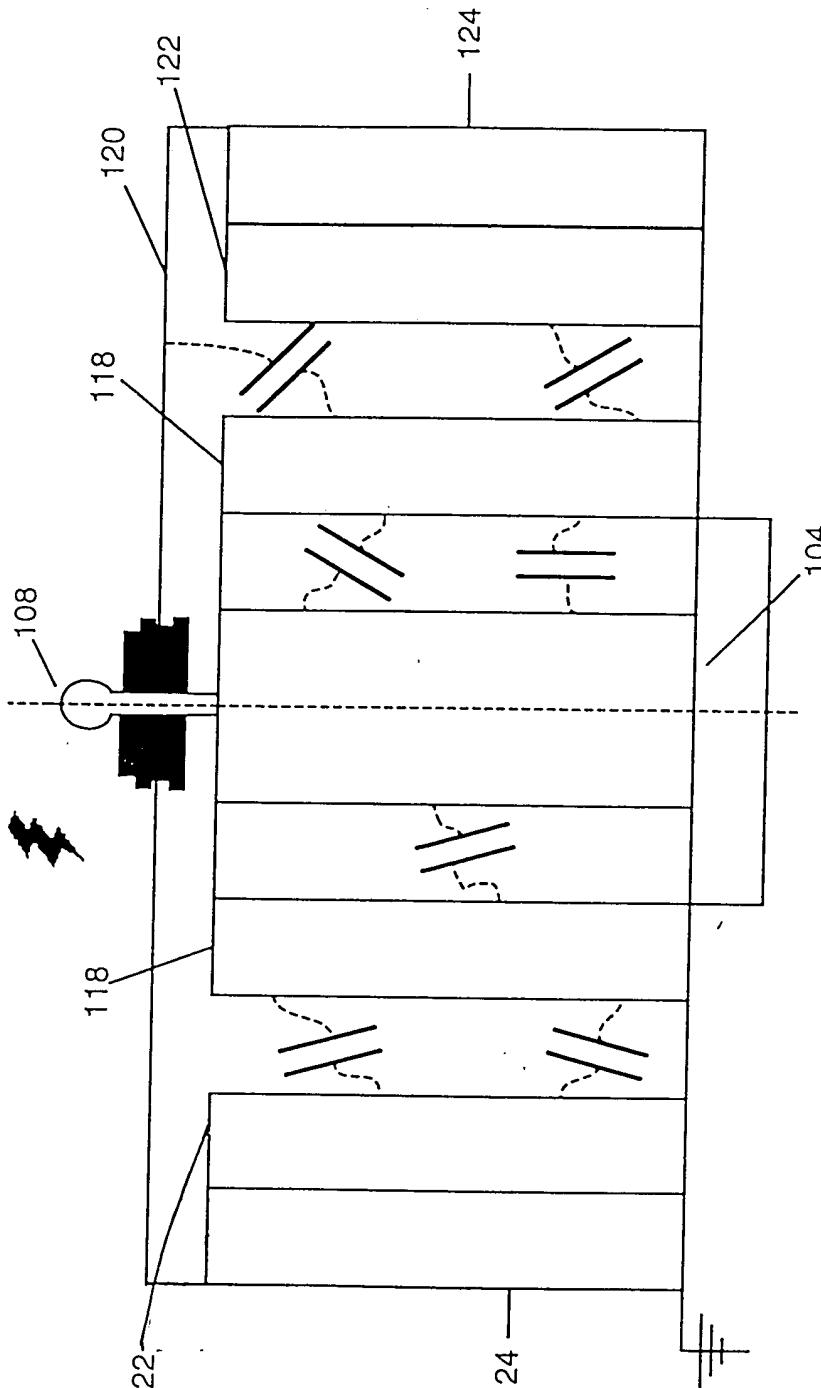


Figure 9



PARAMETERS:

$$C_g = 5n$$

$$C_{sd} = 1.37p$$

$$C_{sg} = 1.09p$$

$$C_{si} = 2.49p$$

$$R_1 = 8Meg$$

$$R_{108} = 8Meg$$

$$R_2 = 8Meg$$

$$R_3 = 8Meg$$

$$R_4 = 8Meg$$

$$R_{24} = 8Meg$$

$$R_{25} = 8Meg$$

$$R_{76} = 1m$$

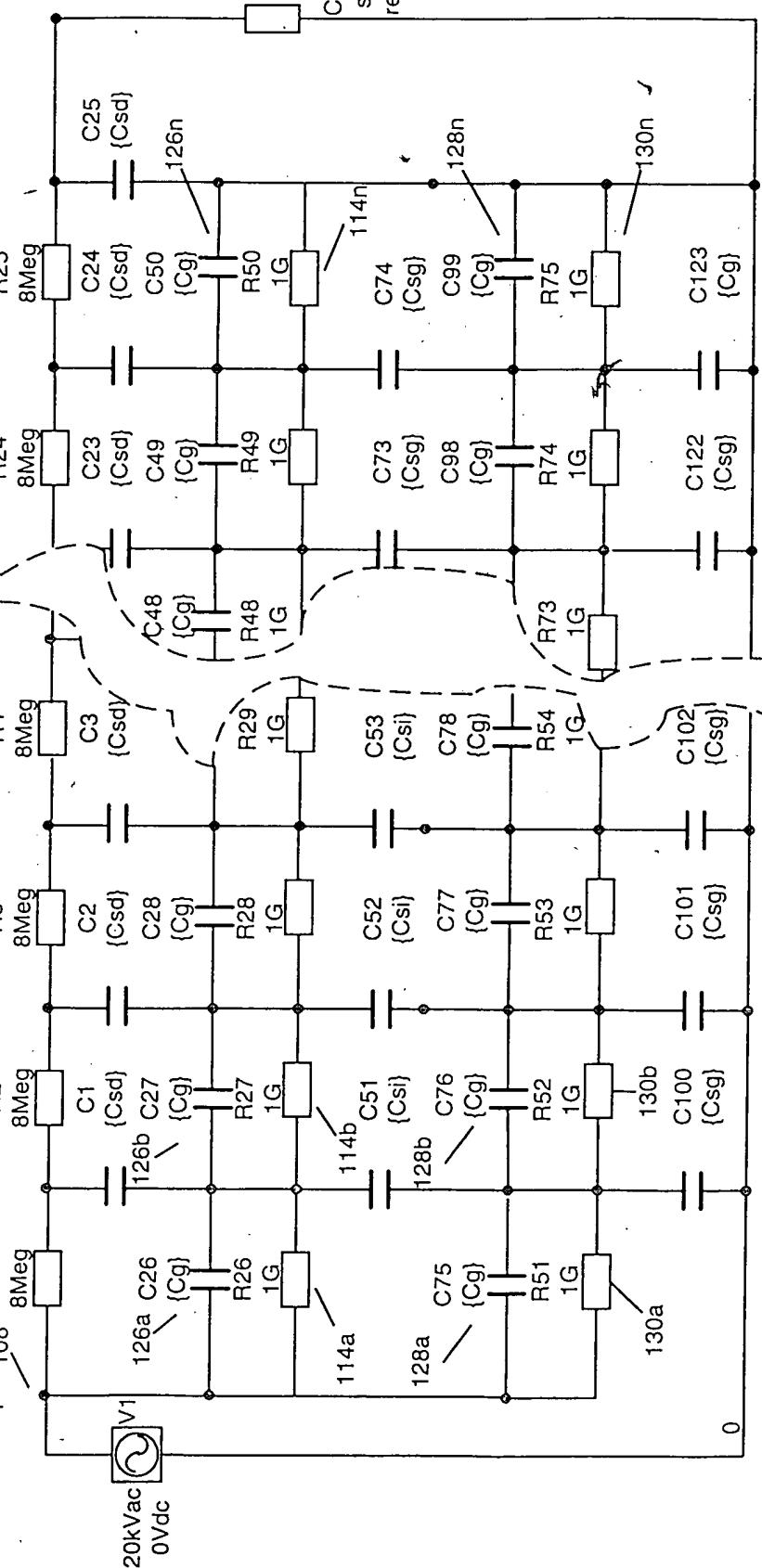
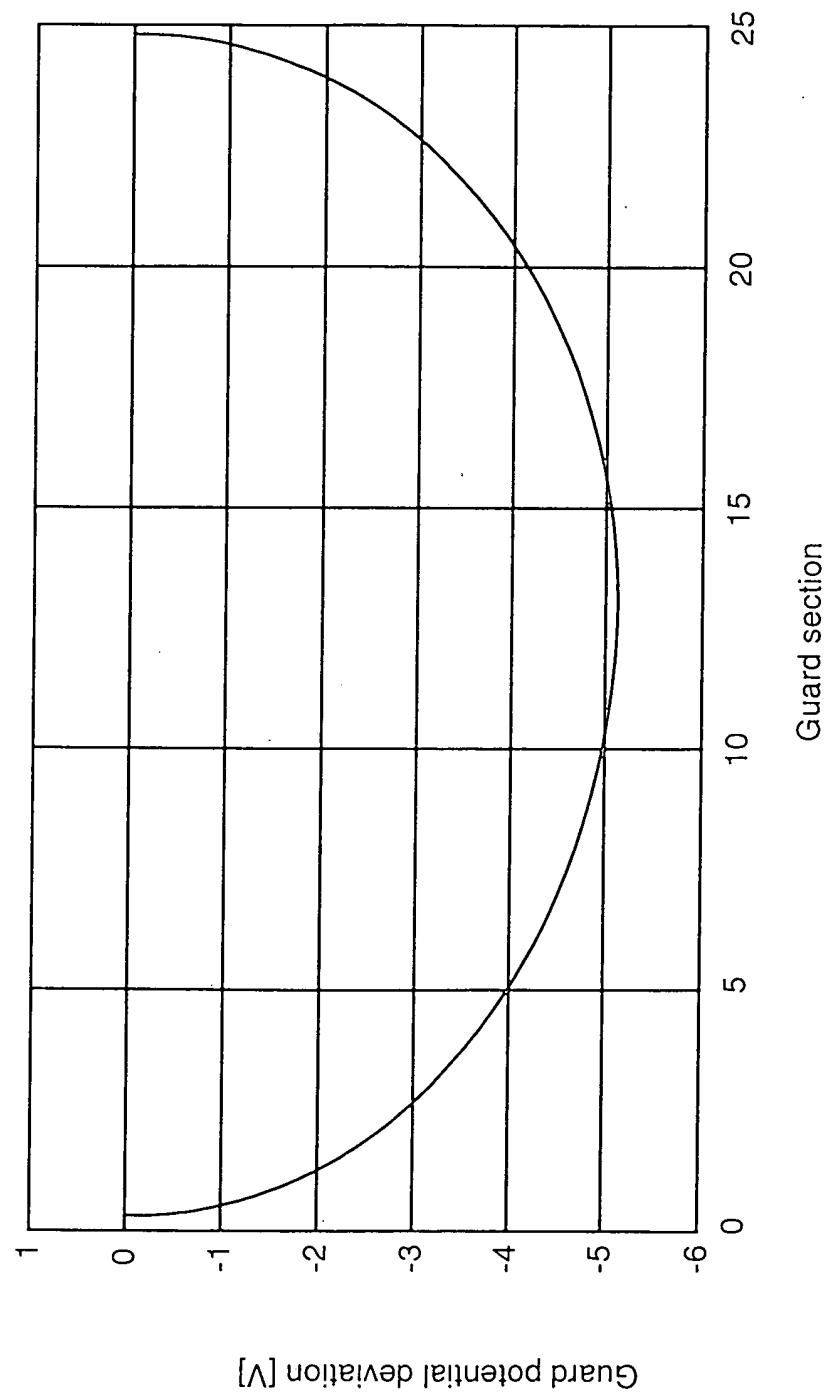
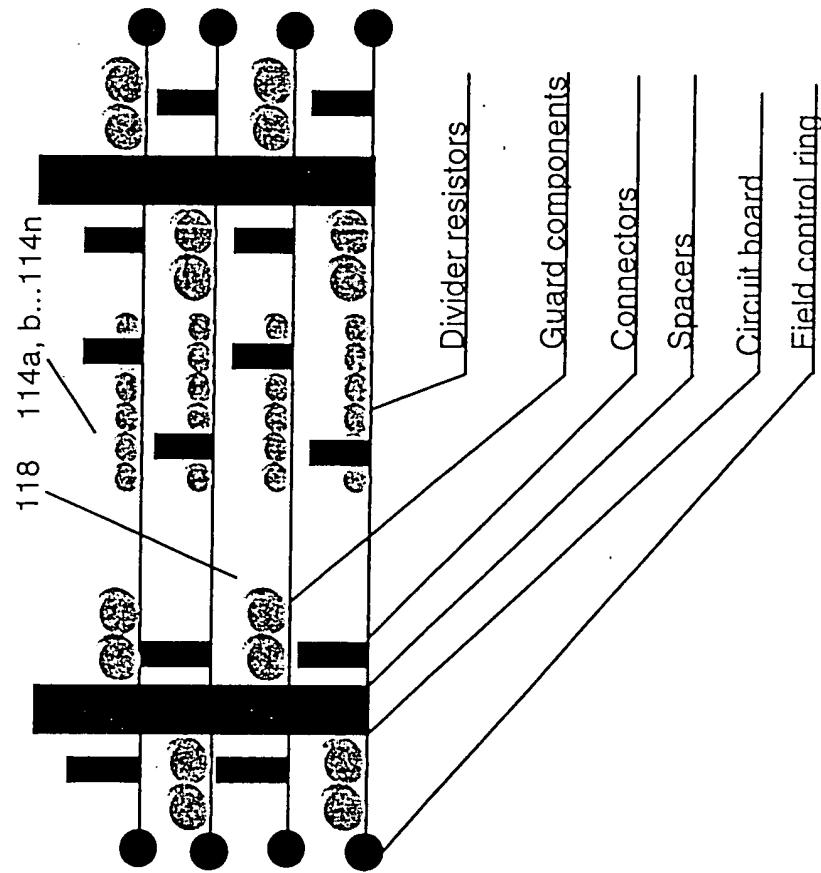


Figure 10



Deviation of the guard potential from ideal potential

Figure 11



Cross-sectional view of four
circuit boards in a divider stack

Figure 12

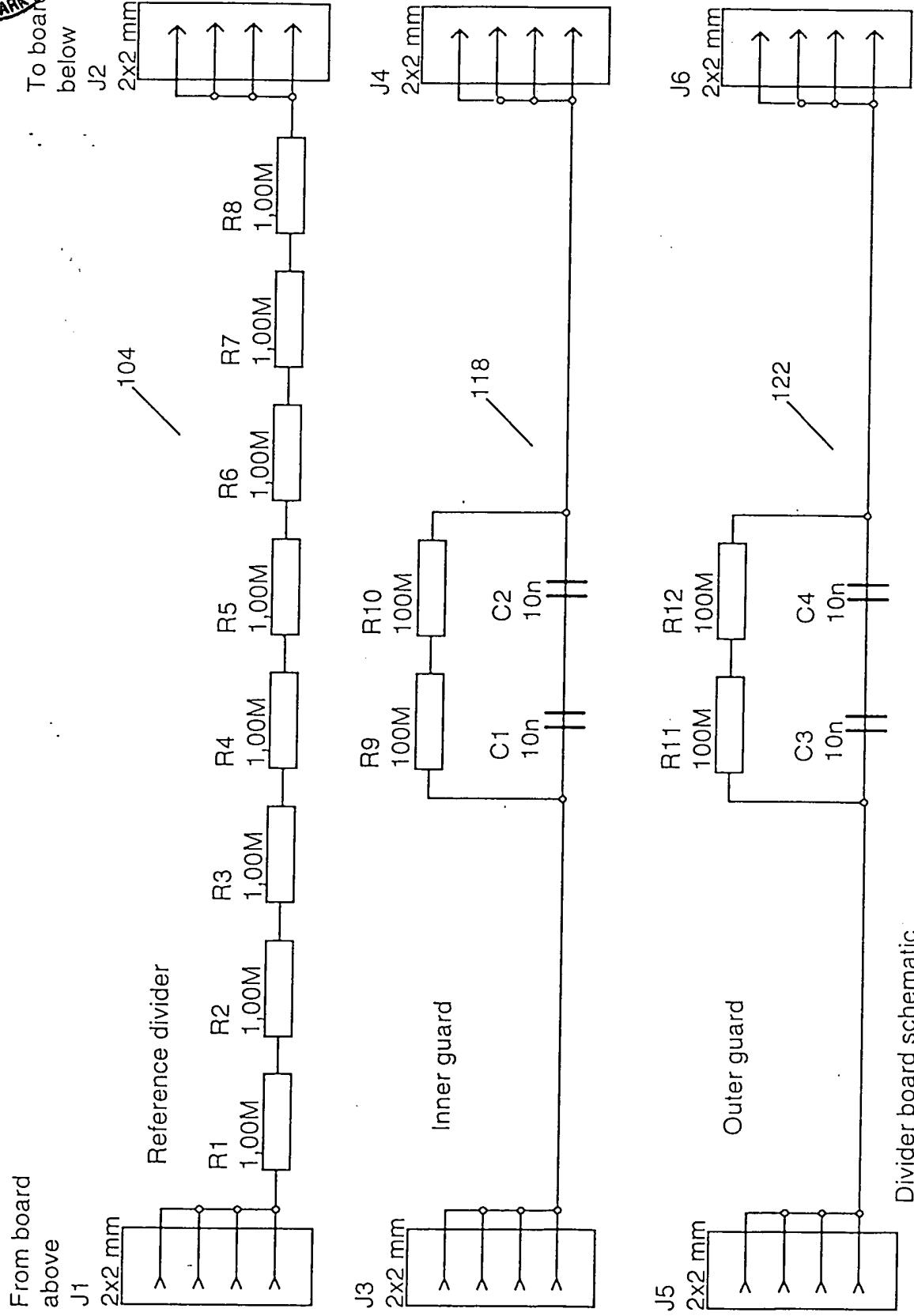


Figure 13

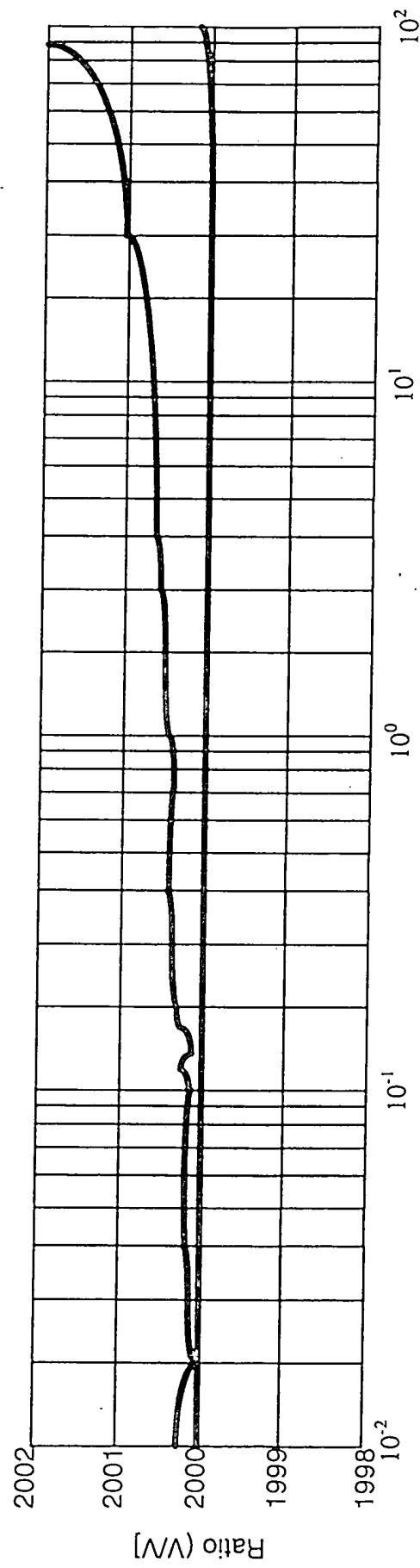


Figure 14



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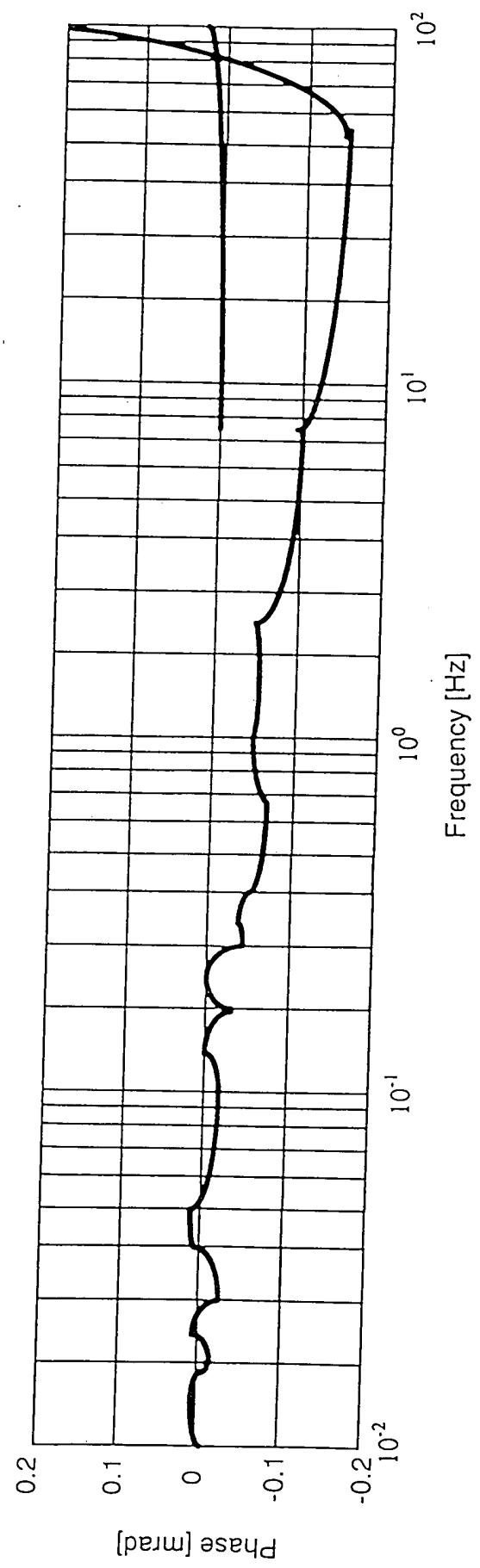


Figure 15

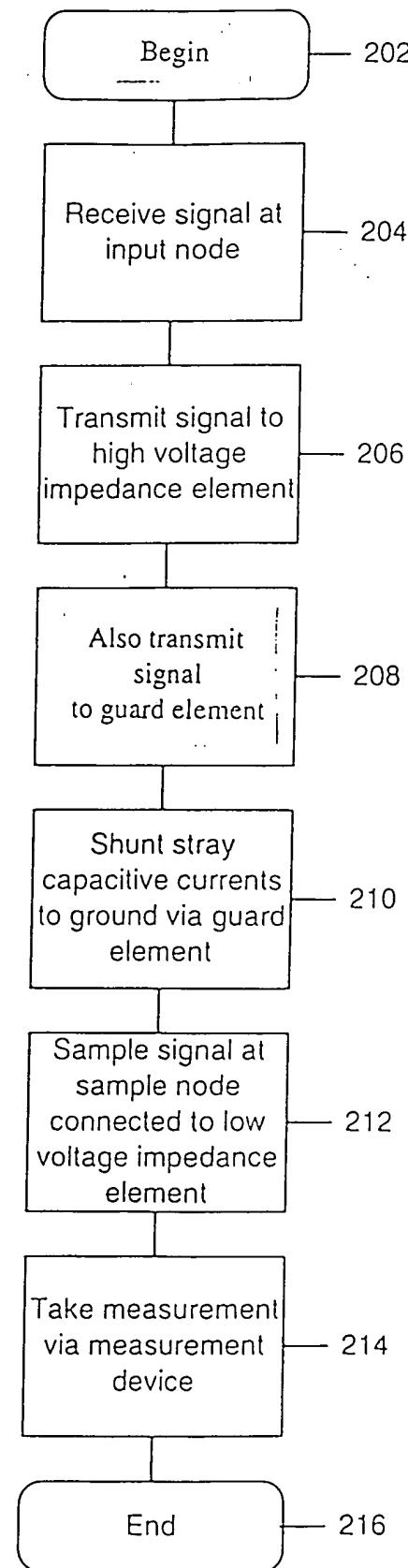


Figure 16